CDX-C8000R/C8000RX

SERVICE MANUAL

Ver 1.2 2001.06

AEP Model UK Model



Photo: CDX-C8000R

• The tuner and CD sections have no adjustments.

Model Name Using Similar Mechanism	CDX-C5000RV
CD Drive Mechanism Type	MG-383V-121//Q
Optical Pick-up Name	KSS-720A

SPECIFICATIONS

CD player section

System Compact disc digital audio
Signal-to-noise ratio 90 dB
Frequency response 10 – 20,000 Hz
Wow and flutter Below measurable limit

Tuner section

FΜ

Tuning range 87.5 – 108.0 MHz
Aerial terminal External aerial connector
Intermediate frequency 10.7 MHz/450 kHz

Usable sensitivity 8 dBf

Selectivity 75 dB at 400 kHz
Signal-to-noise ratio 66 dB (stereo),
72 dB (mono)
Harmonic distortion at 1 kHz

0.6% (stereo),

0.3% (mono) 35 dB at 1 kHz

Separation 35 dB at 1 kHz Frequency response 30 – 15,000 Hz

MW/LW

Tuning range MW: 531 – 1,602 kHz LW: 153 – 279 kHz

Aerial terminal External aerial connector Intermediate frequency 10.7 MHz/450 kHz Sensitivity MW: 30 µV

LW: 40 uV

Power amplifier section

Outputs Speaker outputs (sure seal connectors)
Speaker impedance 4-8 ohms
Maximum power output $50 \text{ W} \times 4 \text{ (at 4 ohms)}$

General

Outputs Audio outputs

Power aerial relay control

Power amplifier control

lead

Telephone ATT control lead

Power requirements 12 V DC car battery (negative ground)

Dimensions Approx. $178 \times 50 \times 183 \text{ mm}$

(w/h/d)

Mounting dimension Approx. $182 \times 53 \times 162 \text{ mm}$

Approx. 1.3 kg

Supplied accessories Parts for installation and

connections (1 set) Front panel case (1)

Design and specifications are subject to change without

notice.

FM/MW/LW COMPACT DISC PLAYER

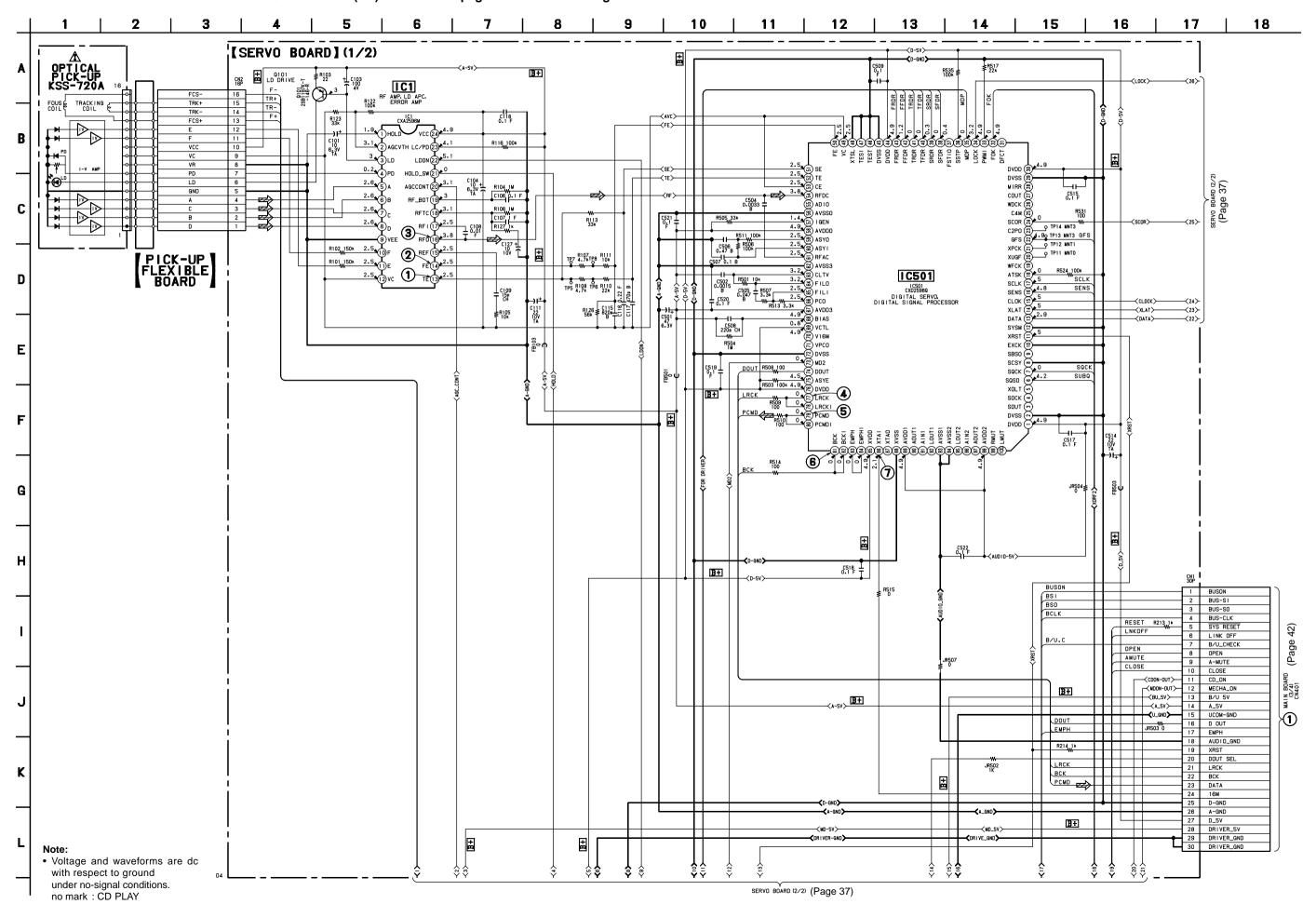
9-870-127-12 Sony Corporation 2001F0400-1 e Vehicle Company

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SONY

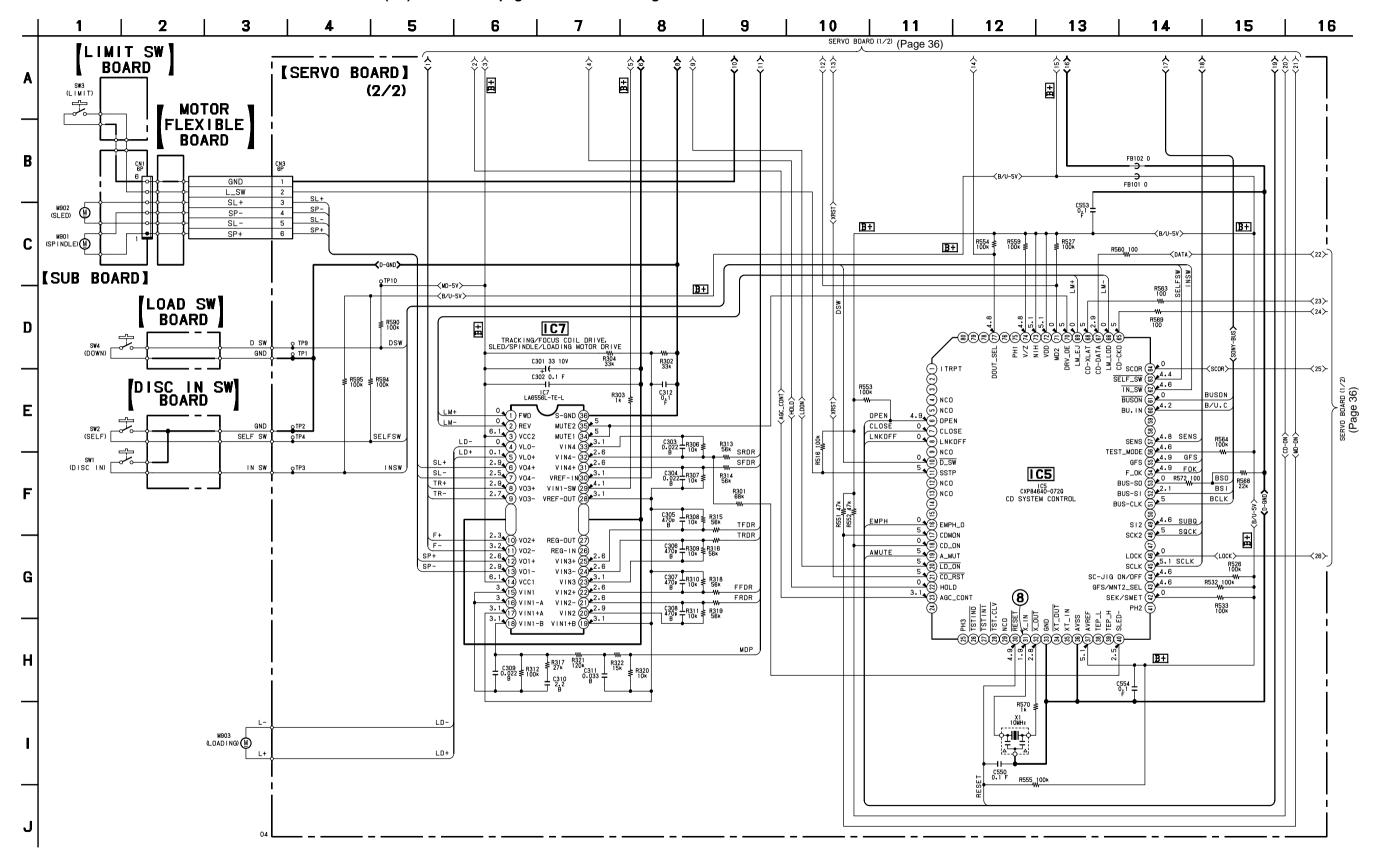
Refer to page 33 for Waveforms.

3-7. SCHEMATIC DIAGRAM — CD MECHANISM SECTION (1/2) — • Refer to page 50 for IC Block Diagrams.



• Refer to page 33 for Waveforms.

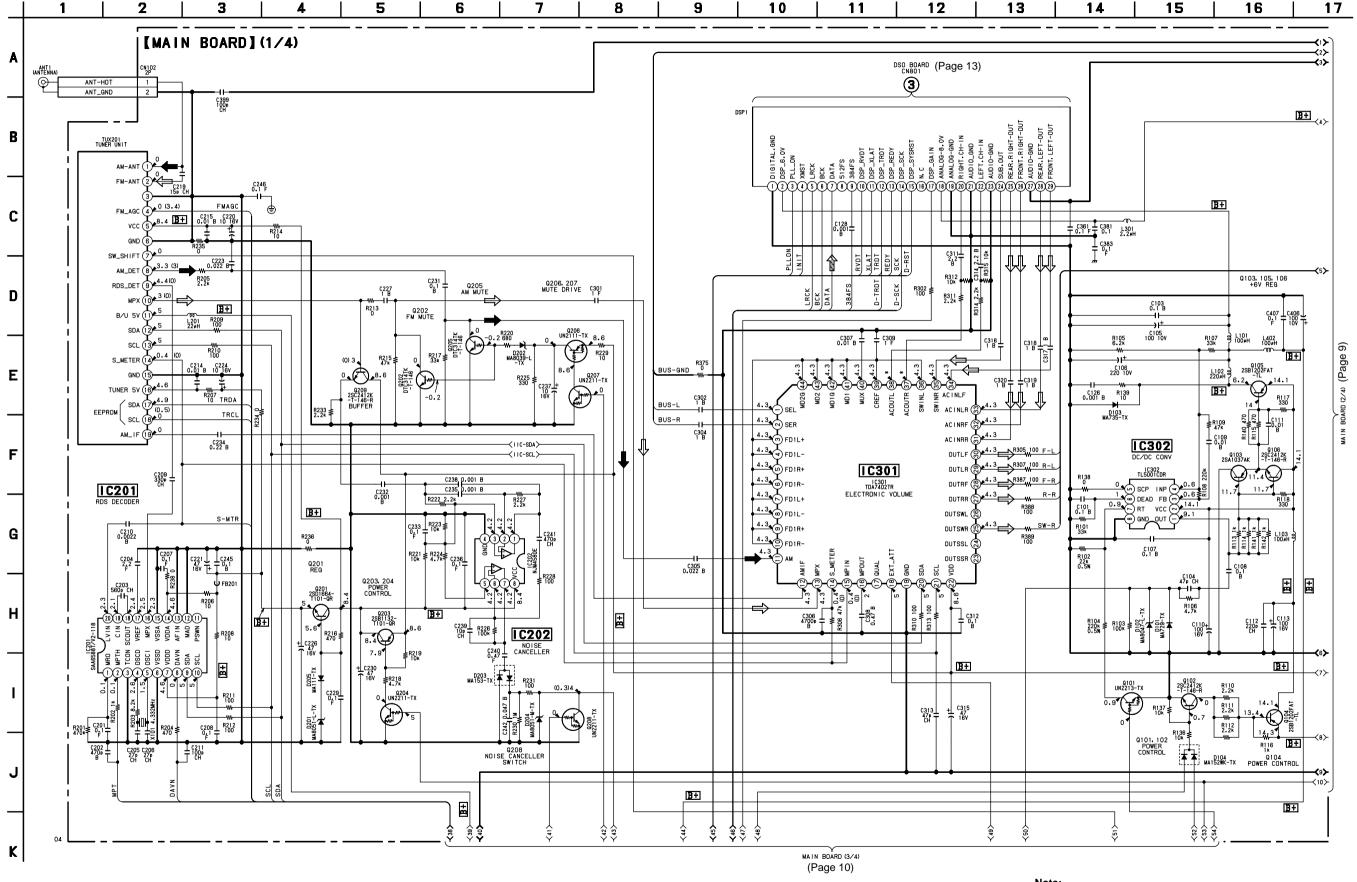
3-8. SCHEMATIC DIAGRAM — CD MECHANISM SECTION (2/2) — • Refer to page 50 for IC Block Diagrams.



Note:

Voltage is dc with respect to ground under no-signal conditions.
 no mark: CD PLAY

2-4. SCHEMATIC DIAGRAM — MAIN SECTION (1/4) — • Refer to page 50 of Service manual for IC Block Diagrams.

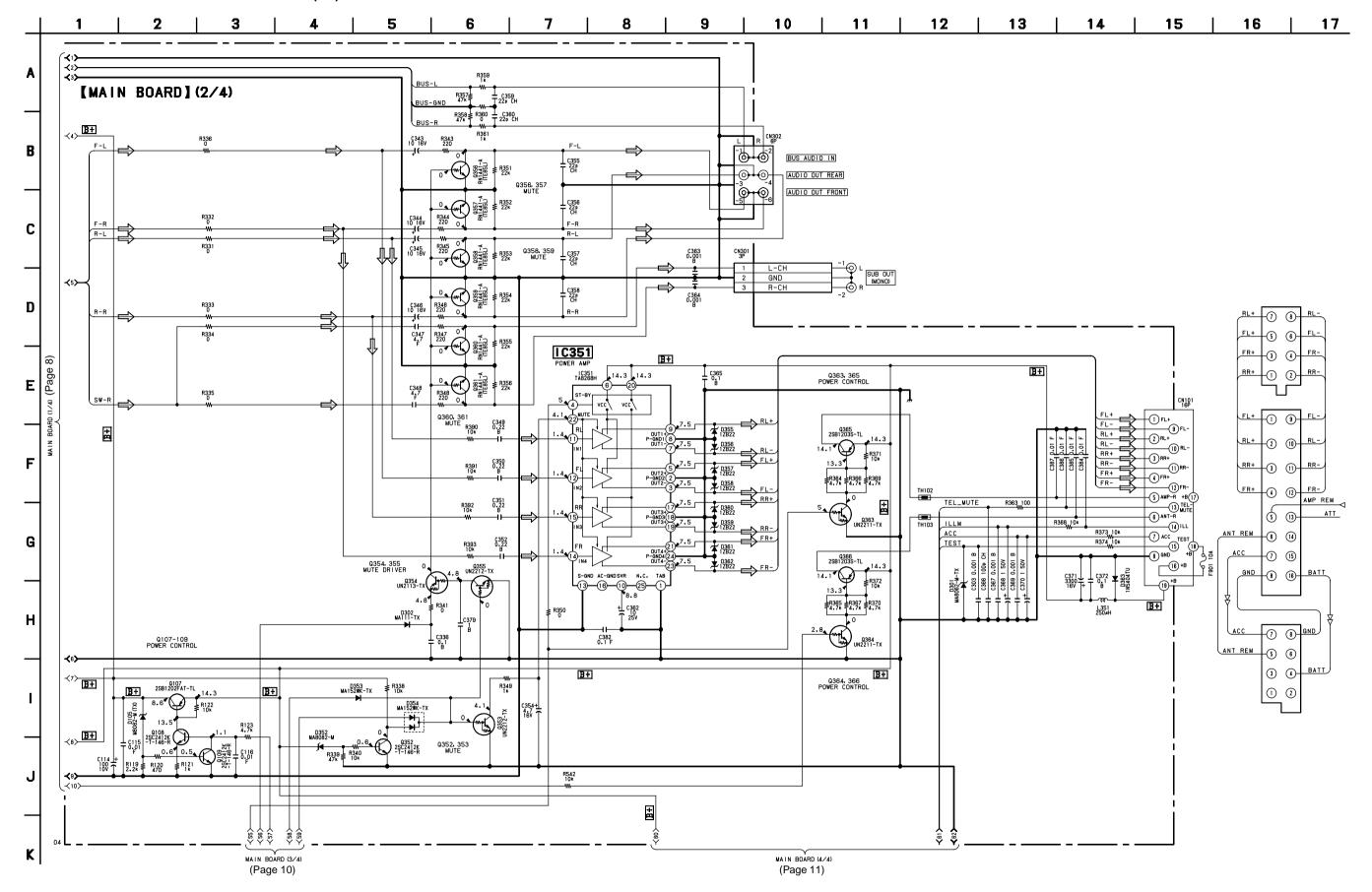


· Voltage is dc with respect to ground under no-signal (detuned) condition. no mark: FM

) : MW

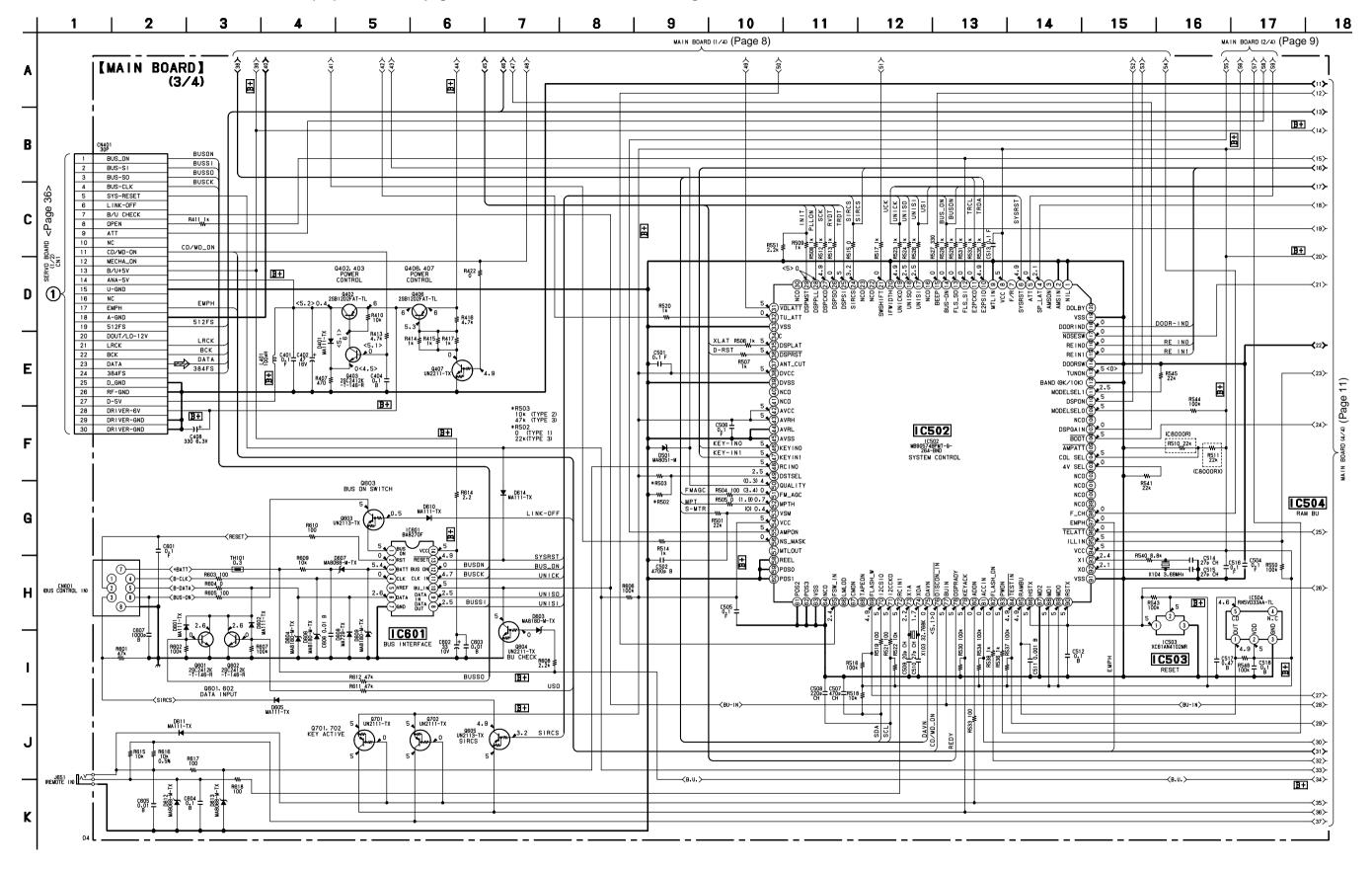
: Impossible to measure

2-5. SCHEMATIC DIAGRAM — MAIN SECTION (2/4) —



Note:

2-6. SCHEMATIC DIAGRAM — MAIN SECTION (3/4) — • Refer to page 50 of Service manual for IC Block Diagrams.

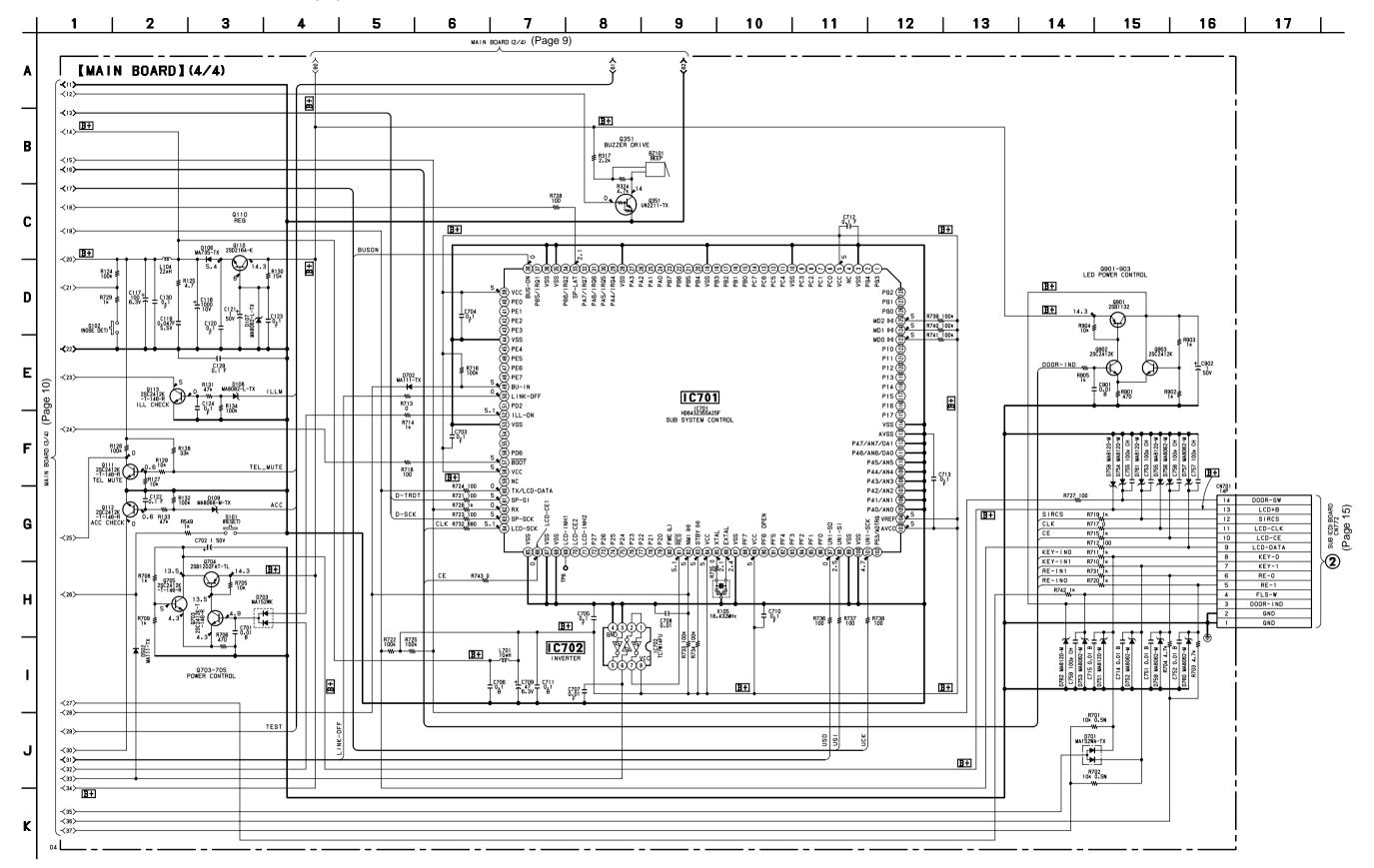


Note:

 Voltage is dc with respect to ground under no-signal (detuned) condition.
 no mark: FM

(): MW < >: CD PLAY

2-7. SCHEMATIC DIAGRAM — MAIN SECTION (4/4) —



Note:

2-9. SCHEMATIC DIAGRAM — DSO SECTION — 6 5 7 8 9 10 11 12 13 14 **≺**T0-EV0L**>** [DSO BOARD] (ANALOG-8V) ≺ANALOG-GND> C847 680 CH **B**+ B+ 1 B+ R858 R842 R847 6.8k 10k В C843 10 16V IC807 DRAMVDD (0) 3.2 31 DA LGND B+ B+ C813 0.1 B 3.3 (32) ADL VDD т. Р 🗐 33) ADI GND R811 + 2.2M C810 22 10V 1.5 (34) ADLREF C814 0.1 B 1.6 35 ADL IN DRAMGND (96) DVDD (95) 3.2 C 3.3 36 DA2GND 3.3 37 DA2VDD C840 10 16V R-L + W 3.3 38 DA2LO 3.3 39 MCKVDD ANALOG DIGITAL R837 R843 6.8k 10k TC805 T.P (92)-PART PART 1.5 41 MCKI 1C805 CXD2726Q-4 €! ≽ R838 ≱ C848 680p CH 0 42 MCKGND 1.5 43 DA2RO 3.3.44 DA2VDD D C816 0.1 B ± C817 0.1 B 45 DA2GND C817 0.1 B 1.6 45 ADRIN C817 0.1 B 1.6 47 ADRREF IC801 PLLVDD 83 3.2 C811 22 10V 3.3 49 ADRVDD 48) ADRGND ₹R848 47k C845 10 16V R849 6.8k 22MHZ (81) R852 10k SUB R806 680 2% E -W-R880 10k R899 -(51)(52)(53)(54)(55)(56)(57)(58)(59)(80)(81)(82)(83)(84)(85)(86)(87)(88)(89)(70)(71)(72)(73)(74)(75)(78)(79)(80) R881 **B**+ C827 I 0.1 B T R876 2200 2200 2200 3.9k R-R R803 100 R815 ≱ B+ B+ ₼ FB801 C801 33 10V R874 ₹ 220p 220p ₹ R875 3.9k ₹ 220p 220p ₹ 3.9k FRONT.LEFT-OUT # REAR.LEFT-OUT Q802 REG R827 ≱ AUD I O-GND FRONT.RIGHT-OUT IC809 RR Q802 2SC2412K-R REAR.RIGHT-OUT B+ SUB **Q** 24 SUB.OUT **B+** <5v> 23 AUDIO-GND G R810 220 ≸ C853 L 680p CH CH R845 R850 6.8k 10k 5.5 22 LEFT.CH-IN ± R826 ≱ ≹R857 10k 21 AUDIO-GND **IC804** RIGHT, CH-IN 20 R859 10k C807 0.1 19 ANALOG-GND PLLON 18 ANALOG-8.0V B+ C842 10 16V DATA -R893 100 17 DSP_GAIN C806 0.1 B 16 N.C IC804 TC7SET08FU 3 FB803 RST 15 DSP SYSRST FB804 FB805 FB806 FB807 Н) NI 5(° SCK 14 DSP_SCK IC808 REDY .6_√(∼)1N2 C822 68 10V 13 DSP REDY B+ TRDT 12 DSP_TRDT IC803 XLAT C826 100 6.3V C FB808 C FB809 C R865 0 DSP_XLAT -11-RVDT I C802 PQ3DZ53U C823 0.1 B 10 DSP RVDT R863 R872 384FS MA111-TX 9 384FS vc 6 512FS R855 C846 680p CH 8 512FS FB811 FB812 FB813 FB814 FB819 DATA vo (m) 3.2 DATA R846 10k C850 680p CH R871 ₹

BCK

LRCK

XMST

+ C804 C805 B+

NC (=)

IC802

GND (-

6 BCK

XMST

PLL-0N

DSP-6.0V

DIGITAL.GND

5 LRCK

• Voltage is dc with respect to ground under no-signal (detuned) condition. no mark: FM

XMST

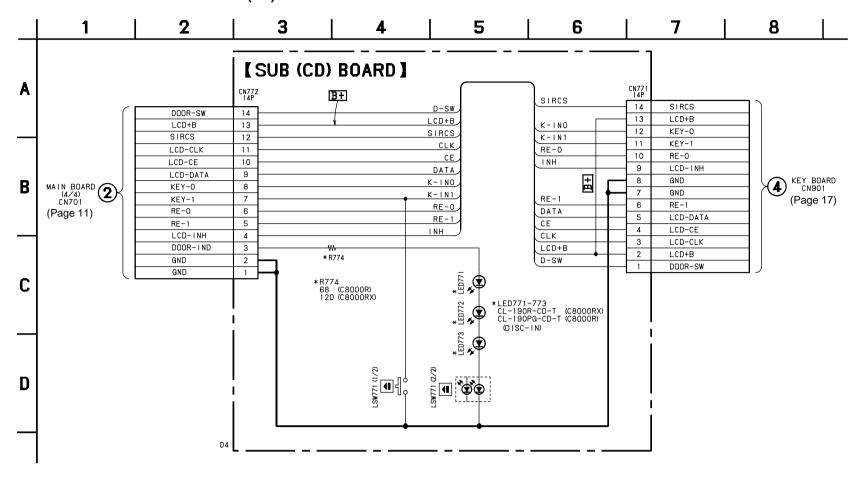
BCK

LRCK

384FS

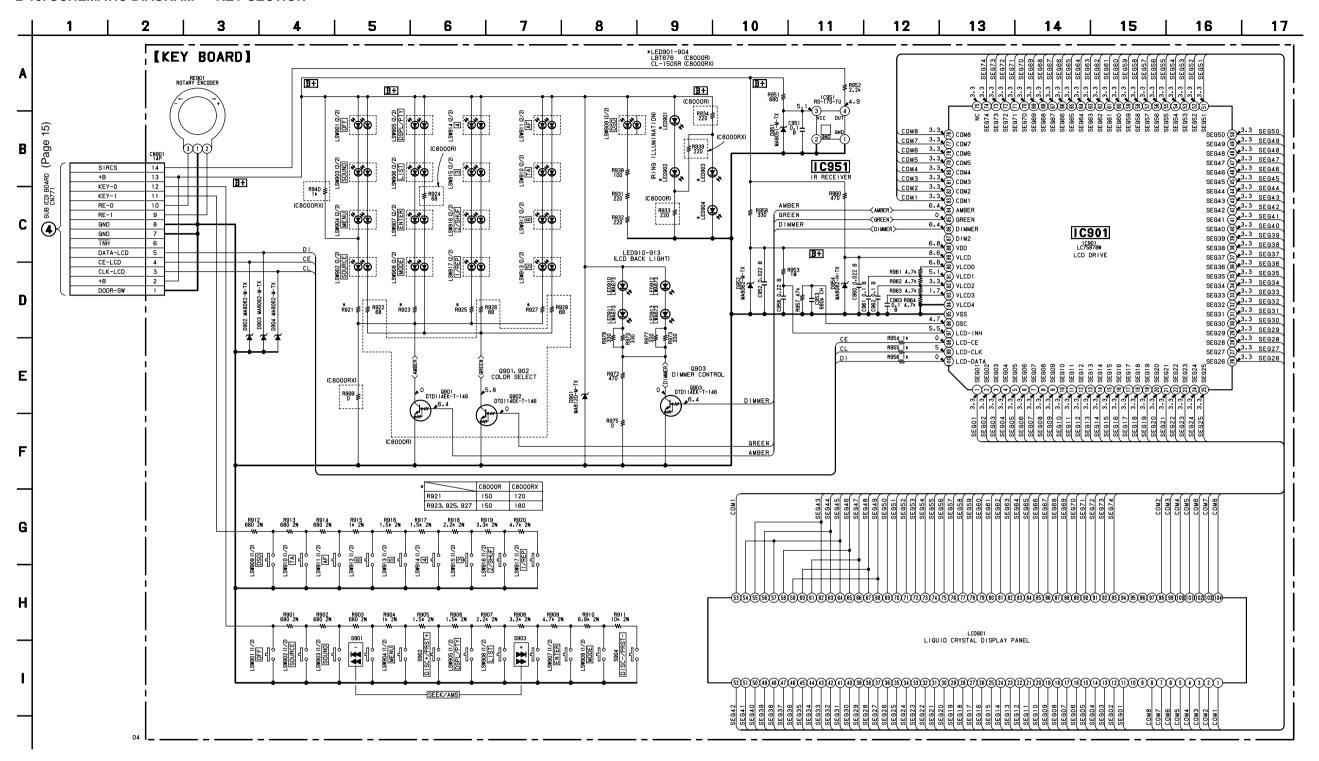
R822 100

2-11. SCHEMATIC DIAGRAM — SUB (CD) SECTION —



Note:

2-13. SCHEMATIC DIAGRAM — KEY SECTION —



Note: